TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of	
Invention	

Accurate Generation of Scan Enable Signal when Testing Integrated Circuits Using Sequential Scanning Techniques

Application Number :

Date

Mr. Hasanuzzaman K SHEIKH

First Named Applicant: Confirmation Number:

Attorney Docket Number: T

TI-36330

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B	P. L.
Documents being submitted:	Files
us-fee-sheet	txnefiling-usfees.xml
us-100-51100(us-fee-sheet.xsl
	us-fee-sheet.dtd
us-ids	txnefiling-usidst.xml
us-ius	us-ids.dtd
	us-ids.xsl
us-request	txnefiling-usrequ.xml
uu roquout	us-request.dtd
	us-request.xsl
application-body	Appltxn72-trans.xml
арричина 333)	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-gname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
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	isocyr2.ent
	isodia.ent
	isolat1.ent
	isolat2.ent
	isonum.ent
	isopub.ent
	mmlextra.ent
	mmlalias.ent
	soextblx.dtd
	figtxn72_1.tif
	figtxn72_2.tif
	figtxn72_3.tif
	figtxn72_4.tif
	figtxn72_5.tif
	figtxn72_6.tif
	figtxn72_7.tif
de de service	figtxn72_8.tif
us-declaration	decltxn72pg1.tif
us-declaration Comments	decltxn72HSpg.tif
Comments	